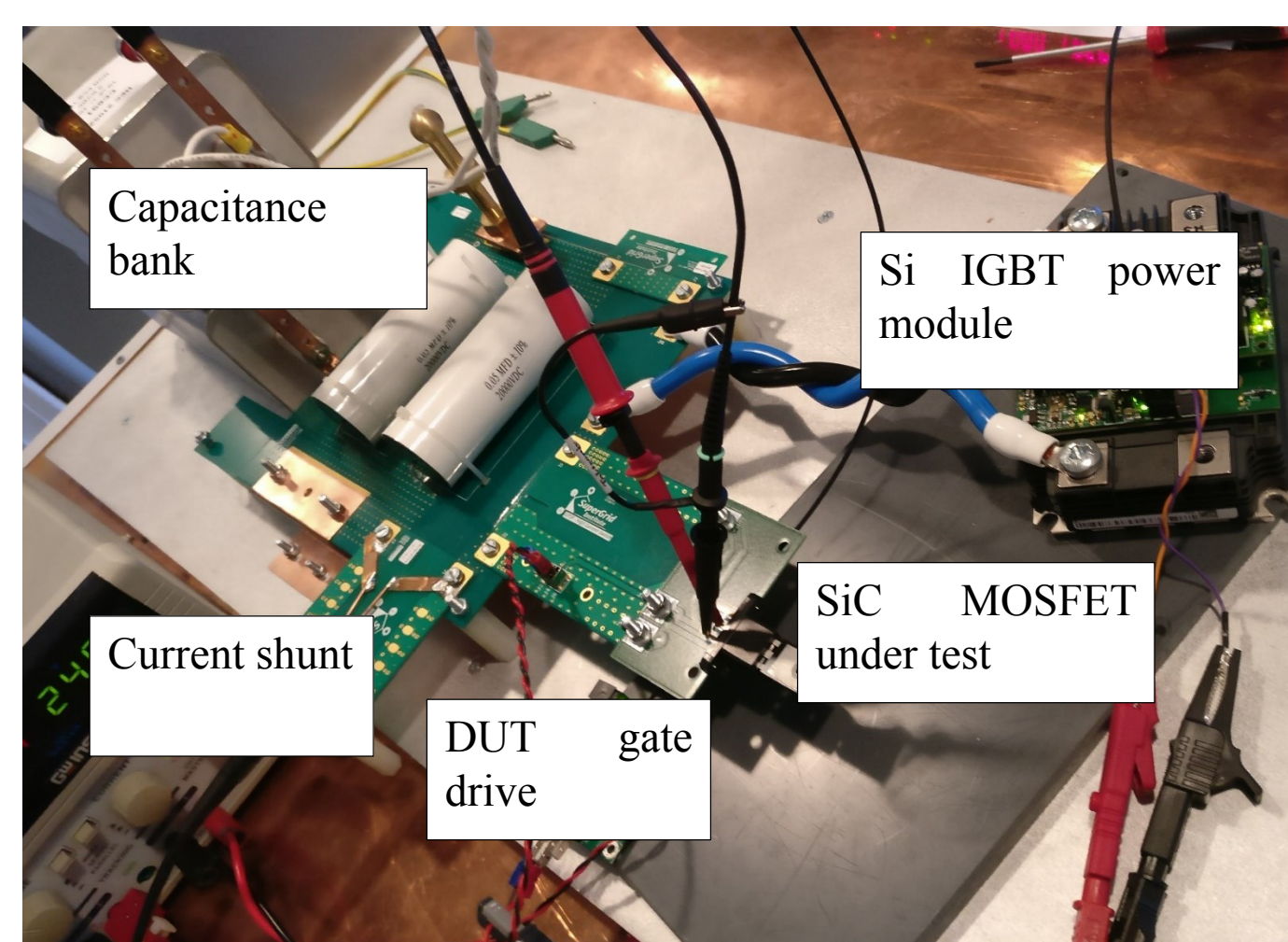


Abstract

Robustness study for the 1.7 kV SiC MOSFET is presented. After evaluation of the critical energy required for failure, devices were submitted to repetitive short-circuits conditions. Because the power switches experienced very stressful mode, the monitoring of key parameters is required to understand failures which in all occurrence are related to the gate oxide weakness. The strong impact of drain to source bias voltage on the critical energy during short-circuit mode is also investigated. Additionally, test bench and protocols are detailed.

Short circuit test bench overall description

- Up to 20 kV short-circuit test
- Devices under test : 1.7 kV 45 mΩ



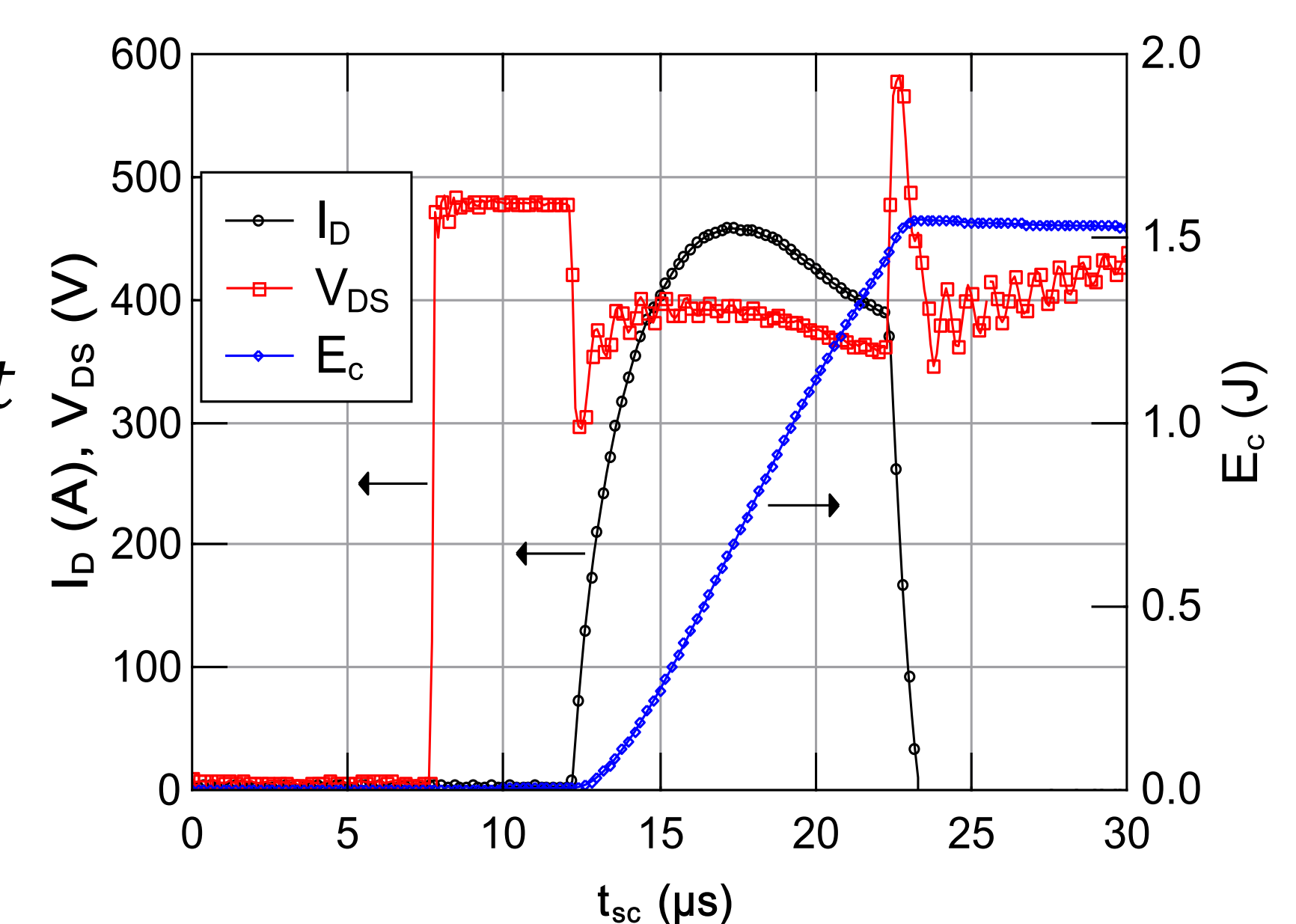
Critical energy estimation

- In order to perform repetitive short-circuit test, the critical energy needs to be evaluated

- $V_{DS} = 400 \text{ V}$

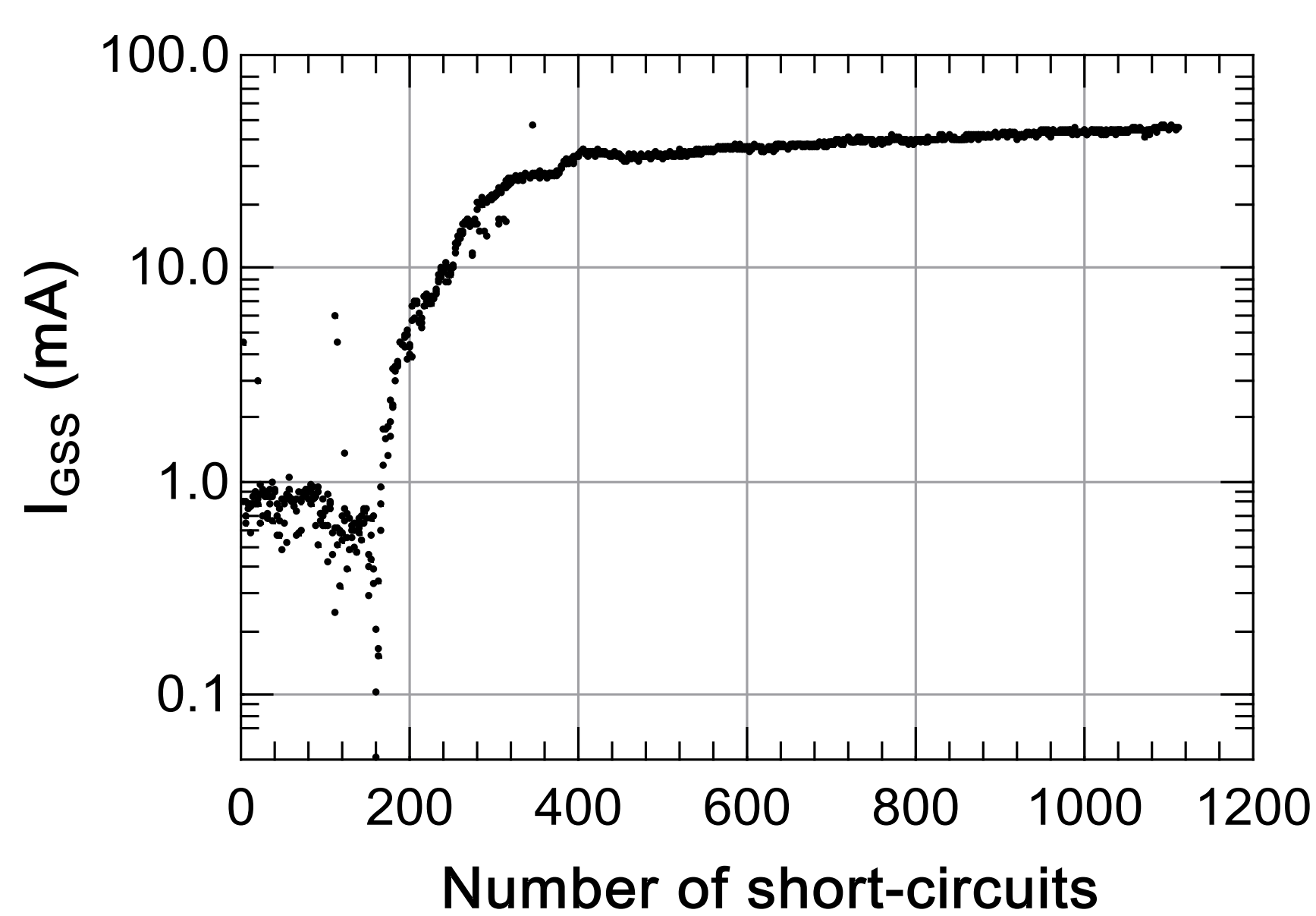
- $I_{SC} \approx 450 \text{ A}$

- $E_C = \int_0^{t_{sc}} V_{DS} \cdot I_{SC} \cdot dt$
 $E_C = 1.5 \text{ J}$



Gate leakage current monitoring

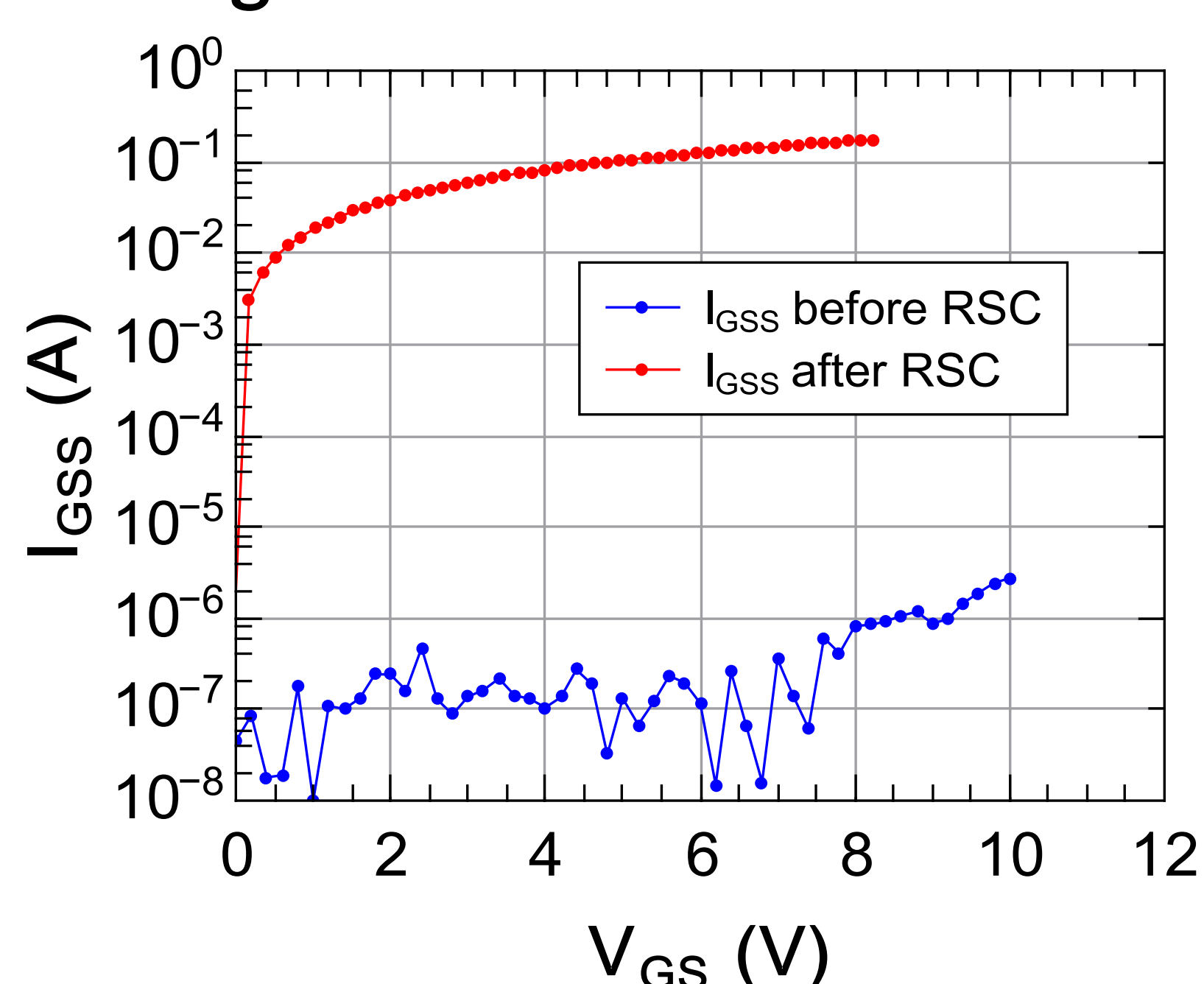
- I_{GSS} monitoring during RSC
- First RSC test at 80%. E_C
 - > gate oxide failure in ~160SC
 - > Gate leakage current increase show gate degradation



- Key parameters changes

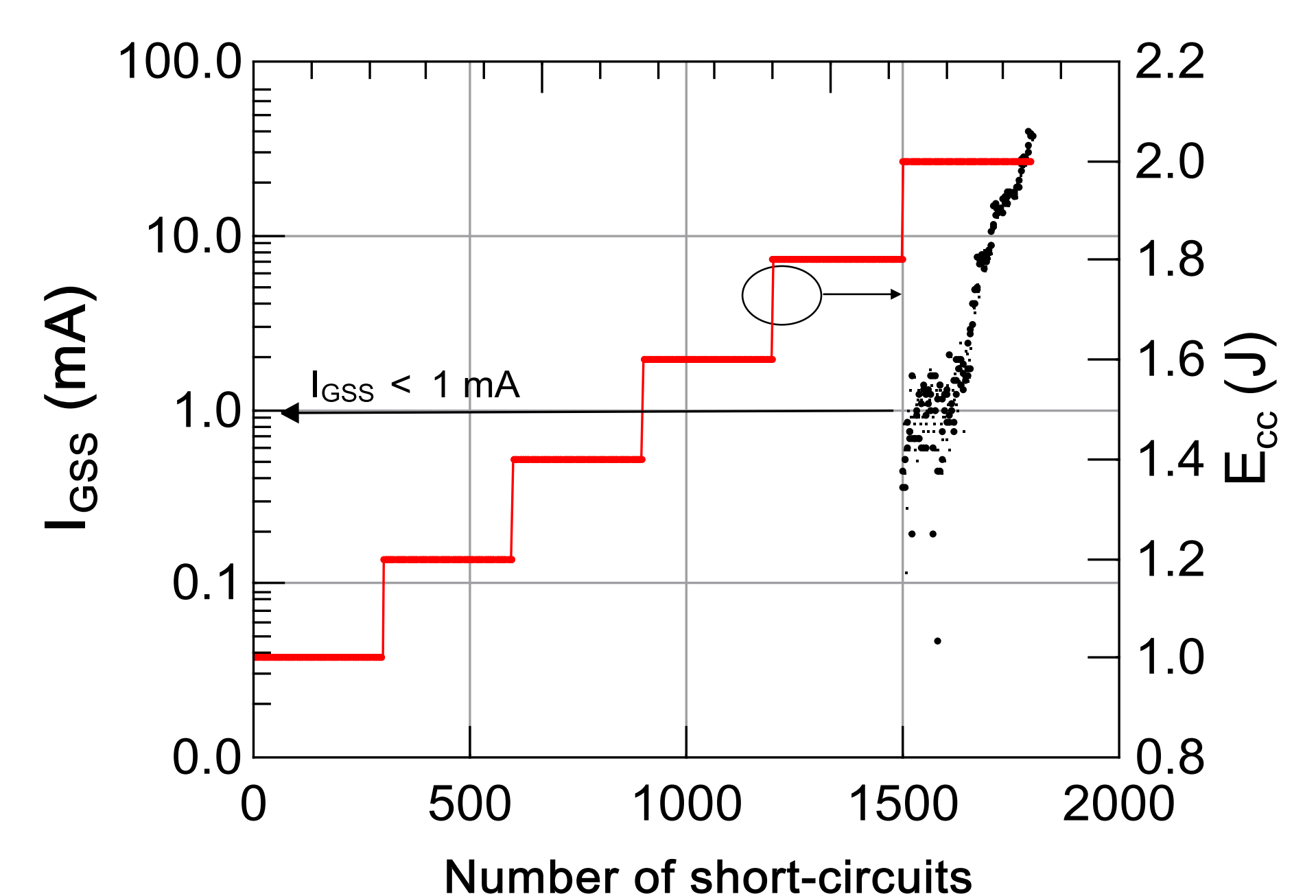
Parameters	Before RSC	After 1000 RSC
I_{GSS}	39.4 pA	>1 mA
V_{th}	2.86 V	2.98 V
R_{DSon}	40 mΩ	80.5 mΩ
R_g	1.95 Ω	19.8 Ω
V_{BR}	2.21 kV	2.21 kV

- Gate leakage current characterization

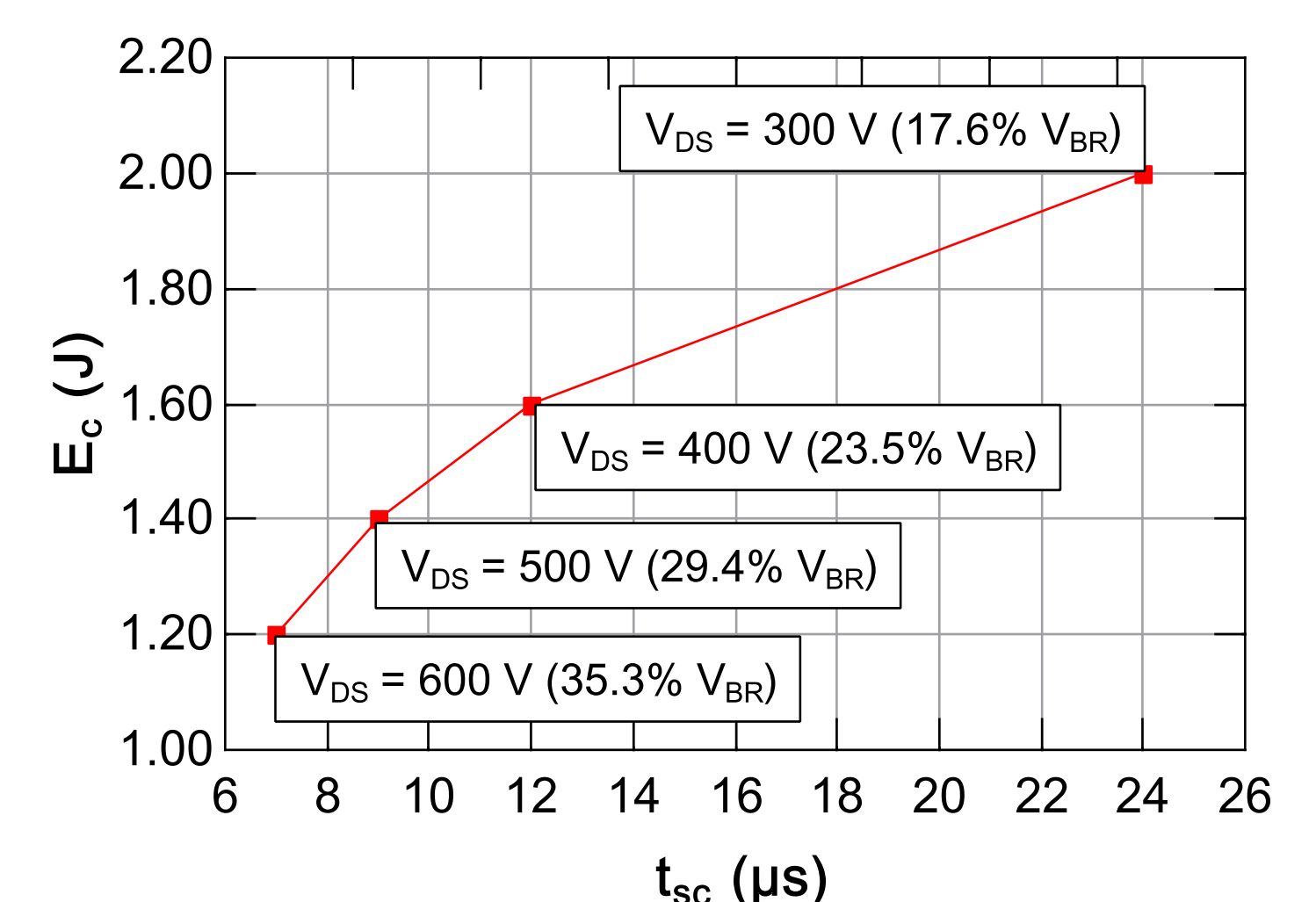
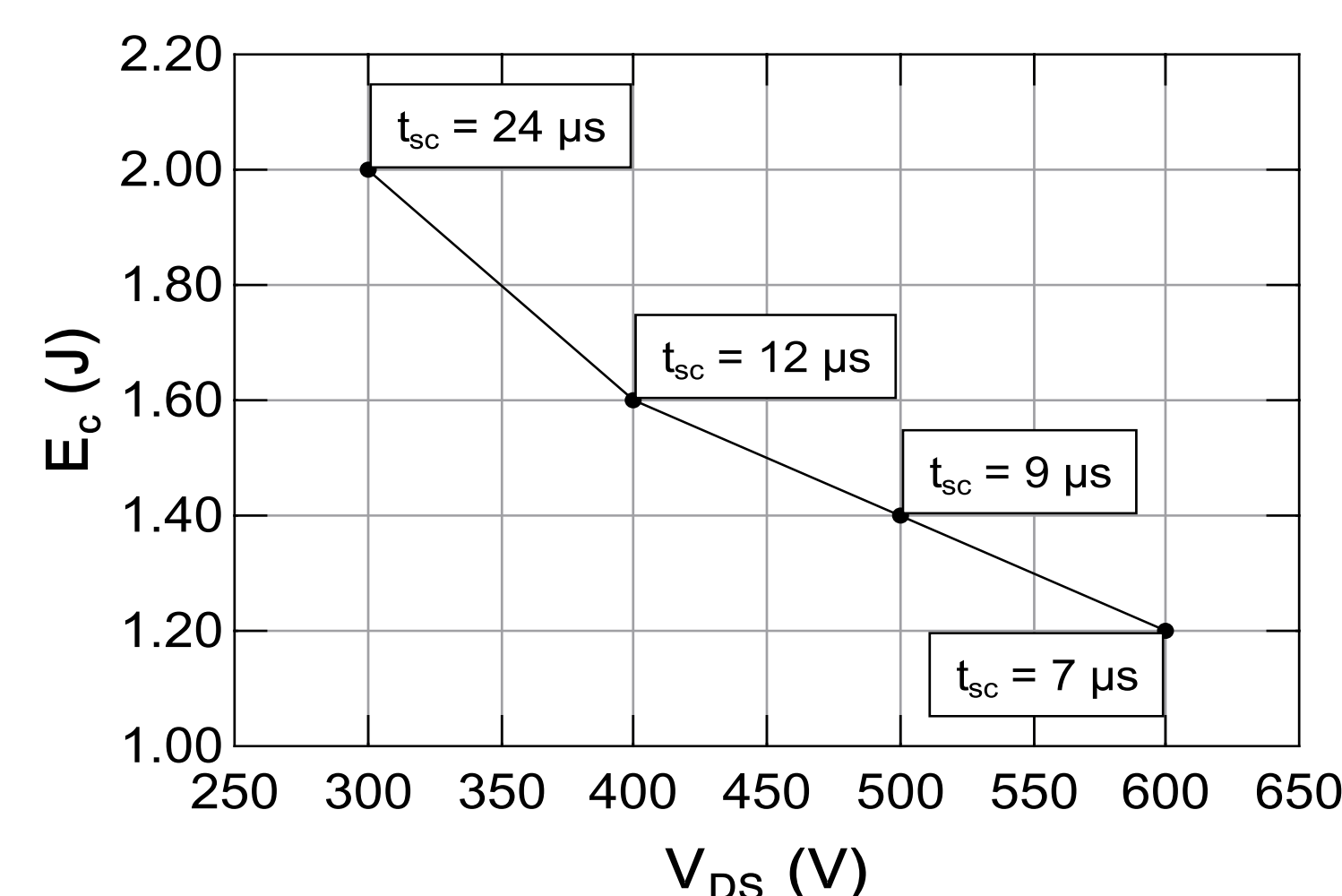


Drain to source voltage impact on critical energy

- Test protocol:
 - > 300SC by E_{SC} steps
 - > E_{SC} increased until failure



- When V_{DS} is increased then the energy required to degrade the gate oxide is decreased



Conclusions

- SiC MOSFET short-circuit withstanding capacity is worrying.
 - > Once 80%. E_C of a component is reached, around a few hundred are needed for gate failure
- If the 80%. V_{BR} condition is respected for 1.7 kV MOSFET
 - > t_{sc} can be approximated to ~3 μs
- There is some critical dispersion on critical energy
 - > $1.5 \text{ J} < E_C < 2 \text{ J}$
 - > Strict measurement protocol are needed